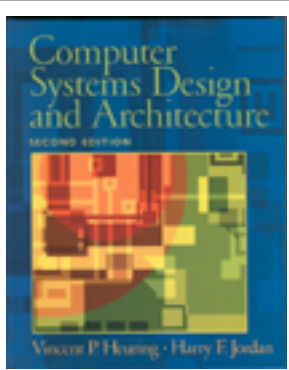




## Computer Organization

CSC-211

Fall 2006

	<p><b>Instructor:</b> Daniel A. Cañas Office: West #249 Phone: 758-5355 Office hours: W 1:00-2:00pm , Tu-Th 12:00-1:00 pm or by appointment E-mail: <a href="mailto:canas@wfu.edu">canas@wfu.edu</a></p> <p><b>Textbook:</b> Heuring V., Jordan H., "Computer Systems Design and Architecture", Prentice Hall , 2004.</p> <p><b>Pre-requisites:</b> CSC-112 and MTH-117</p>
<p><b>Grading:</b> Homework: 20% Exams: 25% Labs: 30% Final Exam: 25%</p>	<p><b>Purpose:</b> This course will introduce the student to computer organization from the perspective of instructions, including the central processor, busses, input and output units, and memory units. The internal sequence of execution of instruction, memory fetching and I/O will be studied.</p>

### TENTATIVE TOPICS

- 1 General Purpose Machines
- 2-Machine, Machine Language and Digital Logic
- 3-Processor Design
- 4-Computer Arithmetic
- 5-Computer Arithmetic and the Arithmetic Unit
- 6-Memory System Design
- 7-Input and Output
- 8-Peripheral Devices

\* If you have a disability that may require an accommodation for taking this course, then please contact the Learning Assistance Center (758-5929) within the first two weeks of the semester.