

Special Topics - CSC 311/611

Computer Architecture

Spring 2008

Instructor: Daniel A. Cañas

Office hours: T-TH 2-3 pm

Objectives:

This course will introduce the student to parallel and multiprocessor architectures, including MIMD, SIMD, pipeline and vector architectures.

Requirements:

CSC 211 Computer Organization

Evaluation:

Exam 1	25%
Exam 2	25%
Homework	20%
Project	30%

Graduate students: will be required extra reading from current literature and make a class presentation.

Contents:

- 1- Computer Arithmetic
- 2-Pipeline and Vector Processing
- 3-Input-Output Organization
- 4-Memory Organization
- 5-Multiprocessors
- 6-Case studies